

HDL - LAB

VLSI CAD - LAB

REVALUATION RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I **EXAMINATION HELD IN NOVEMBER 2015** Course : Revised Course - 2013 COLLEGE: GOA COLLEGE OF ENGINEERING

Seat No: 3101	PRNo: 201507401	S	ex: M	Nan	ne:	BHASALE VINAY	BHANUDAS	=
No Of Attempts: 1 Solid State De	evices and Semiconductor P	hveice	No Of Credits	Gra Obta		SGPA		
	The and commondation	4.5	.30					
		Theory	4	BB	Р			
		IA	2	BB	Ρ			
Digital VLSI D	esign							
		Theory	4	BC	Р			
		IA	2	BB	Р			
Analog VLSI D	esign							
		Theory	4	CC	P			
		IA	2	AB	Р			
VLSI Technolo	gy			A. 10	100			
		Theory	4	ВВ	Р			
		IA	2	BB	Р			
Advanced Eng	ineering Mathematics				(\$P)			

2

2

2

2

2

38

BC

CC P

AB

AA

BB Р

ВВ Ρ

6.68 P

Theory

Practical

Practical

Total:

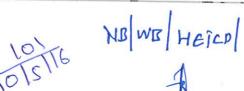
IA

IA

IA

Seat No: 3102	PRNo: 201107681	Se	ex: M	Nan	ne ·	PASSES BORKAR NEEL RAMAKANT
No Of Attempts: 1			No Of Credits	Gra Obta	de	
Solid State De	evices and Semiconductor	· Physics				
		Theory	4	BB	P	
		IA	2	AB	P	
Digital VLSI D	esign					
		Theory	4	CC	Ρ	
		IA	2	AB	Р	
Analog VLSI	Design					
		Theory	4	AB	Р	
		IA	2	AB	Р	
VLSI Technolo	gy					
		Theory	4	AB	Р	
		IA	2	AB	Р	
Advanced Eng	ineering Mathematics					
		Theory	4	BC	Р	
		IA	2	ВС	Р	
HDL - LAB						
		IA	2	AB	Р	
		Practical	2	ВВ	Р	
VLSI CAD - LA	В				15	
		IA	2	BB	Р	
		Practical	2	ВС	Р	
		Total:	38	***		7.05 P PASSES





P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average



COLLEGE: GOA COLLEGE OF ENGINEERING Seat No: 3103 PR No: 201108381 Sex: F Name: GOVEKAR DIVYA DIGAMBAR No Of Attempts: 1 No Of Grade SGPA Credits Obtained Solid State Devices and Semiconductor Physics Theory 4 CC Ρ 2 вв Р Digital VLSI Design Theory 4 F FF IA 2 CC Р Analog VLSI Design Theory 4 CC Р IA 2 ВВ Ρ VLSI Technology Theory 4 CC Р IA 2 BC Р Advanced Engineering Mathematics Theory 4 CC P IA 2 BC P HDL - LAB IA 2 P AA Practical 2 AB Р VLSI CAD - LAB 2 BB Р Practical 2 BC P Total: 38 5.32 F FAILS Seat No: 3105 PR No: 200907827 Sex: F Name: KHANDEPARKAR SHWETA MAHENDRA No Of Attempts: 1 No Of Grade **SGPA** Credits Obtained Solid State Devices and Semiconductor Physics Theory 4 P AB IA 2 AB P Digital VLSI Design Theory Р 4 AB 2 IA Р AB Analog VLSI Design Theory 4 P BB 2 IA P AB VLSI Technology Theory 4 P AB 2 IA P AB Advanced Engineering Mathematics Theory Р 4 BB IA 2 AB Ρ HDL - LAB IA 2 P AA Practical 2 P AA VLSI CAD - LAB IA 2 AB P Practical 2 BB P

Total:

38

7.84 P PASSES

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average



Seat No: 3106 PR	No: 201008499	Sex	: F	Nan	ne :	KHARANGATE LAXIMI SHASHANK
No Of Attempts: 1			No Of Credits	Gra Obta		SGPA
Solid State Devices ar	nd Semiconductor					
		Theory	4	AB	Р	
		IA	2	AA	Р	
Digital VLSI Design						
		Theory	4	BB	P	
A I > // O D '		IA	2	AB	Ρ	
Analog VLSI Design		Theony	4	۸۵	Б	
		Theory IA	4 2	AB	P P	
VLSI Technology		IA	2	AB	Р	
VEST Technology		Theory	4	AB	Р	
		IA	2	AA	Р	
Advanced Engineering	Mathematics	iF	2	~~		
Advanced Engineering	matromation	Theory	4	ВС	Р	
		IA	2	BB	P	
HDL - LAB		,,	-	55	•	
		IA	2	AA	Р	
		Practical	2	AB	P	
VLSI CAD - LAB						
		IA	2	AB	Р	
		Practical	2	AB	Р	
		Total:	38	AME		7.79 P
2-4N- 2400 DDA	I 004407707	0-				PASSES
seat No: 3108 PRN lo Of Attempts: 1	lo: 201107707	Sex	: M			MAHATME MOHIT BHAIRAV
o of Attempts . T			No Of	Grad		SGPA
Solid State Devices an	d Semiconductor I	Physics	Credits	Obtai	nea	001 A
		Theory	4	вс	Р	
		IA	2	BB	P	
Digital VLSI Design				0.770.000	8	
_		Theory	4	CC	Р	
		IA	2	CC	Р	
Analog VLSI Design						
		Theory	4	BC	Ρ	
		IA	2	BC	Ρ	
VLSI Technology						
		Theory	4	CC	Ρ	
		1110019	7			
		IA	2	ВВ	Р	
Advanced Engineering	Mathematics	IA	2			
Advanced Engineering	Mathematics	IA Theory	2	CC	P P	
	Mathematics	IA	2			
Advanced Engineering HDL - LAB	Mathematics	IA Theory IA	2 4 2	CC BC	P P	
	Mathematics	Theory IA	2 4 2	CC BC	P P	
HDL - LAB	Mathematics	IA Theory IA	2 4 2	CC BC	P P	
	Mathematics	Theory IA IA Practical	2 4 2 2 2	CC BC AA AB	P P P	
HDL - LAB	Mathematics	Theory IA	2 4 2	CC BC	P P	

Total:

38

6.16 P PASSES

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average



at No : 3109	PRNo:	201007353	Se	x: F	Nan	ne :	MAYEKAR MITHILI NARAYAN
Of Attempts: 1				No Of Credits	Gra Obta	de	SGPA
Solid State D	evices and S	emiconductor	Physics	Orcans	Obla	iiicu	
			Theory	4	BC	Ρ	
			IA	2	BC	Р	
Digital VLSI I	Design						
			Theory	4	CC	P	
			IA	2	CC	Ρ	
Analog VLSI	Design						
			Theory	4	CC	P	
			IA	2	BC	P	
VLSI Techno	logy						
			Theory	4	CC	P	
			IA	2	CC	P	
Advanced En	gineering Ma	thematics					
			Theory	4	FF	F	
			IA	2	CC	P	
HDL - LAB							
			IA	2	AB	Р	
			Practical	2	BB	P	
VLSI CAD - L	AB						
			IA	2	BC	Р	
					00		
			Practical	2	CC	Р	
			Practical Total:	38		Р	5.00 F
MNo 2110	D.D.No.	204006208	Total:	38	_		FAILS
at No: 3110	PRNo:	201006308	Total:	38 c: F	Nam	e:	5.00 F FAILS NAIK STHRIGDHARA NAVANATH
Of Attempts: 1			Total:	38	_	e:	FAILS
Of Attempts: 1		201006308 emiconductor F	Total: Sex	38 c: F No Of	Nam Grad Obtai	e : de ned	FAILS NAIK STHRIGDHARA NAVANATH
Of Attempts: 1			Total: Sex Physics Theory	38 c: F No Of	Nam Grac Obtai BC	e: de ned	FAILS NAIK STHRIGDHARA NAVANATH
Of Attempts: 1 Solid State De	evices and Se		Total: Sex	38 C: F No Of Credits	Nam Grad Obtai	e : de ned	FAILS NAIK STHRIGDHARA NAVANATH
Of Attempts: 1	evices and Se		Total: Sex Physics Theory IA	38 C: F No Of Credits 4 2	Nam Grad Obtai BC AB	de : de ned P	FAILS NAIK STHRIGDHARA NAVANATH
Of Attempts: 1 Solid State De	evices and Se		Sex Physics Theory IA	38 C: F No Of Credits 4 2 4	Nam Grac Obtai BC AB	de : de ned P P	FAILS NAIK STHRIGDHARA NAVANATH
Of Attempts: 1 Solid State Do	evices and So Design		Total: Sex Physics Theory IA	38 C: F No Of Credits 4 2	Nam Grad Obtai BC AB	de : de ned P	FAILS NAIK STHRIGDHARA NAVANATH
Of Attempts: 1 Solid State De	evices and So Design		Total: Sex Physics Theory IA Theory IA	38 C: F No Of Credits 4 2 4 2	Nam Grad Obtai BC AB AB	e: de ned P P	FAILS NAIK STHRIGDHARA NAVANATH
Of Attempts: 1 Solid State Do	evices and So Design		Total: Sex Physics Theory IA Theory IA Theory	38 C: F No Of Credits 4 2 4 2	Nam Grac Obtai BC AB AB BC	e: de ned P P P	FAILS NAIK STHRIGDHARA NAVANATH
Of Attempts: 1 Solid State Do Digital VLSI D Analog VLSI I	evices and Se Design Design		Total: Sex Physics Theory IA Theory IA	38 C: F No Of Credits 4 2 4 2	Nam Grad Obtai BC AB AB	e: de ned P P	FAILS NAIK STHRIGDHARA NAVANATH
Of Attempts: 1 Solid State Do	evices and Se Design Design		Total: Sex Physics Theory IA Theory IA Theory IA	38 C: F No Of Credits 4 2 4 2	Nam Grac Obtai BC AB AB BC	e: de ned P P P P	FAILS NAIK STHRIGDHARA NAVANATH
Of Attempts: 1 Solid State Do Digital VLSI D Analog VLSI I	evices and Se Design Design		Total: Sex Physics Theory IA Theory IA Theory IA Theory	38 C: F No Of Credits 4 2 4 2 4 4	Nam Grac Obtai BC AB AB BC	e: de ned PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	FAILS NAIK STHRIGDHARA NAVANATH
Of Attempts: 1 Solid State De Digital VLSI De Analog VLSI I VLSI Technol	evices and Se Design Design ogy	emiconductor F	Total: Sex Physics Theory IA Theory IA Theory IA	38 C: F No Of Credits 4 2 4 2	Nam Grac Obtai BC AB AB BC	e: de ned P P P P	FAILS NAIK STHRIGDHARA NAVANATH
Of Attempts: 1 Solid State Do Digital VLSI D Analog VLSI I	evices and Se Design Design ogy	emiconductor F	Total: Sex Physics Theory IA Theory IA Theory IA Theory IA	38 C: F No Of Credits 4 2 4 2 4 2	Nam Grace Obtai BC AB AB BC CC BB BC BB	e: de ned P P P P P P P	FAILS NAIK STHRIGDHARA NAVANATH
Of Attempts: 1 Solid State De Digital VLSI De Analog VLSI I VLSI Technol	evices and Se Design Design ogy	emiconductor F	Total: Sex Physics Theory IA Theory IA Theory IA Theory IA Theory IA Theory	38 C: F No Of Credits 4 2 4 2 4 2 4 4 2	Nam Graco Obtai BC AB AB BC CC BB BC BB	e: de ned PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	FAILS NAIK STHRIGDHARA NAVANATH
Of Attempts: 1 Solid State Do Digital VLSI D Analog VLSI I VLSI Technol Advanced Eng	evices and Se Design Design ogy	emiconductor F	Total: Sex Physics Theory IA Theory IA Theory IA Theory IA	38 C: F No Of Credits 4 2 4 2 4 2	Nam Grace Obtai BC AB AB BC CC BB BC BB	e: de ned P P P P P P P	FAILS NAIK STHRIGDHARA NAVANATH
Of Attempts: 1 Solid State De Digital VLSI E Analog VLSI I VLSI Technol	evices and Se Design Design ogy	emiconductor F	Total: Sex Physics Theory IA Theory IA Theory IA Theory IA Theory IA	38 C: F No Of Credits 4 2 4 2 4 2 4 2	Nam Grac Obtai BC AB AB BC CC BB BC CC CC CC CC	e: de ned PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	FAILS NAIK STHRIGDHARA NAVANATH
Of Attempts: 1 Solid State Do Digital VLSI D Analog VLSI I VLSI Technol Advanced Eng	evices and Se Design Design ogy	emiconductor F	Total: Sex Physics Theory IA Theory IA Theory IA Theory IA Theory IA IA Theory IA	38 C: F No Of Credits 4 2 4 2 4 2 4 2 4 2	Nam Grac Obtai BC AB AB BC CC BB BC BB BC BB	e: de ned PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	FAILS NAIK STHRIGDHARA NAVANATH
Of Attempts: 1 Solid State Do Digital VLSI D Analog VLSI I VLSI Technol Advanced Eng	evices and Se Design Design ogy gineering Mat	emiconductor F	Total: Sex Physics Theory IA Theory IA Theory IA Theory IA Theory IA	38 C: F No Of Credits 4 2 4 2 4 2 4 2	Nam Grac Obtai BC AB AB BC CC BB BC CC CC CC CC	e: de ned PPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	FAILS NAIK STHRIGDHARA NAVANATH
Of Attempts: 1 Solid State Do Digital VLSI D Analog VLSI I VLSI Technol Advanced Eng	evices and Se Design Design ogy gineering Mat	emiconductor F	Total: Sex Physics Theory IA	38 C: F No Of Credits 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4	Nam Grac Obtai BC AB AB BC CC BB BC CC BB BC BB	e: de ned PP PP PP PP	FAILS NAIK STHRIGDHARA NAVANATH
Of Attempts: 1 Solid State Do Digital VLSI D Analog VLSI I VLSI Technol Advanced Eng	evices and Se Design Design ogy gineering Mat	emiconductor F	Total: Sex Physics Theory IA Theory IA Theory IA Theory IA Theory IA Theory IA IA Theory IA	38 C: F No Of Credits 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4	Nam Grac Obtai BC AB AB BC CC BB BC CC BB BC BB BB	e: de ned PP	FAILS NAIK STHRIGDHARA NAVANATH
Of Attempts: 1 Solid State Do Digital VLSI D Analog VLSI I VLSI Technol Advanced Eng	evices and Se Design Design ogy gineering Mat	emiconductor F	Total: Sex Physics Theory IA	38 C: F No Of Credits 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4	Nam Grac Obtai BC AB AB BC CC BB BC CC BB BC BB	e: de ned PP PP PP PP	FAILS NAIK STHRIGDHARA NAVANATH

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average



eat No: 3112 PR No: 201107719	Se Se	x: M	Nan	ne: F	PATIL ABHIJEET VILAS
o Of Attempts: 1		No Of Credits	Gra Obta		SGPA
Solid State Devices and Semiconducto	r Physics	Oredits	Obla	iiieu	
	Theory	4	FF	F	
	IA	2	BC	Ρ	
Digital VLSI Design					
	Theory	4	FF	F	
	IA	2	CC	Р	
Analog VLSI Design					
	Theory	4	FF	F	
\" O = 1	IA	2	BC	Р	
VLSI Technology	 ,			_	
	Theory	4	CC	Р	
Advanced Engineering Mathematics	IA	2	CC	Р	
Advanced Engineering Mathematics	Theony	4	00	Г	
	Theory IA	4 2	CC	P P	
HDL - LAB	IA	2		٢	
HOL-LAD	IA	2	AB	Р	
	Practical	2	BB	Р	
VLSI CAD - LAB	Tactical	2	ы	Г	
VEOLONIE END	IA	2	ВВ	Р	
		2	CC	P	
	Practical				
	Practical Total		/MAN W		3.80 F
	Total:	38	/##### NA		3.89 F FAILS
eat No : 3115 P R No : 201007394	Total:		/ Marie 1		3.89 F FAILS SHETGAONKAR ANAY ANIL
	Total:	38 c: M	Nam	e: S	FAILS
o Of Attempts: 1	Total :	38	/ Marie 1	e: S	FAILS
	Total: Sex	38 C: M No Of Credits	Nam Grad Obtai	e: S de ned	FAILS SHETGAONKAR ANAY ANIL
Of Attempts: 1	Total : Sex r Physics Theory	38 C: M No Of Credits	Nam Grac Obtai	e: Side ned	FAILS SHETGAONKAR ANAY ANIL
Of Attempts: 1 Solid State Devices and Semiconducto	Total: Sex	38 C: M No Of Credits	Nam Grad Obtai	e: S de ned	FAILS SHETGAONKAR ANAY ANIL
Of Attempts: 1	Total : Sex r Physics Theory IA	38 No Of Credits 4 2	Nam Grad Obtai FF BC	e: Side ned F	FAILS SHETGAONKAR ANAY ANIL
Of Attempts: 1 Solid State Devices and Semiconducto	Total : Sex r Physics Theory IA Theory	38 No Of Credits 4 2 4	Nam Grad Obtai FF BC	ie: Side ned F P	FAILS SHETGAONKAR ANAY ANIL
Of Attempts: 1 Solid State Devices and Semiconducto Digital VLSI Design	Total : Sex r Physics Theory IA	38 No Of Credits 4 2	Nam Grad Obtai FF BC	e: Side ned F	FAILS SHETGAONKAR ANAY ANIL
o Of Attempts: 1 Solid State Devices and Semiconducto	Total: Sex r Physics Theory IA Theory IA	38 No Of Credits 4 2 4 2	Nam Grad Obtai FF BC FF CC	e: Side ned FPFP	FAILS SHETGAONKAR ANAY ANIL
Of Attempts: 1 Solid State Devices and Semiconducto Digital VLSI Design	Total: Sex r Physics Theory IA Theory IA Theory	38 No Of Credits 4 2 4 2	Nam Grad Obtai FF BC FF CC	e: Side ned FPFPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	FAILS SHETGAONKAR ANAY ANIL
Solid State Devices and Semiconducto Digital VLSI Design Analog VLSI Design	Total: Sex r Physics Theory IA Theory IA	38 No Of Credits 4 2 4 2	Nam Grad Obtai FF BC FF CC	e: Side ned FPFP	FAILS SHETGAONKAR ANAY ANIL
Of Attempts: 1 Solid State Devices and Semiconducto Digital VLSI Design	Total: Sex r Physics Theory IA Theory IA Theory IA	38 No Of Credits 4 2 4 2	Nam Grac Obtai FF BC FF CC	e: Side ned FPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	FAILS SHETGAONKAR ANAY ANIL
Solid State Devices and Semiconducto Digital VLSI Design Analog VLSI Design	Total: Sex r Physics Theory IA Theory IA Theory IA Theory	38 No Of Credits 4 2 4 2 4 4	Nam Grac Obtai FF BC FF CC	e: S de ned F P P P	FAILS SHETGAONKAR ANAY ANIL
Solid State Devices and Semiconducto Digital VLSI Design Analog VLSI Design VLSI Technology	Total: Sex r Physics Theory IA Theory IA Theory IA	38 No Of Credits 4 2 4 2	Nam Grac Obtai FF BC FF CC	e: Side ned FPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPPP	FAILS SHETGAONKAR ANAY ANIL
Of Attempts: 1 Solid State Devices and Semiconducto Digital VLSI Design Analog VLSI Design	Total: Sex r Physics Theory IA Theory IA Theory IA Theory IA	38 No Of Credits 4 2 4 2 4 2	Nam Graco Obtai FF BC FF CC CC CC CC CC	e: Side ned FP FP FP	FAILS SHETGAONKAR ANAY ANIL
Solid State Devices and Semiconducto Digital VLSI Design Analog VLSI Design VLSI Technology	Total: Sex r Physics Theory IA Theory IA Theory IA Theory IA Theory IA Theory	38 No Of Credits 4 2 4 2 4 2 4 4 4 4 4 4 4 4 4 4 4 4 4	Nam Graco Obtai FF BC FF CC CC CC FF CC	e: Side ned FP FP FP FP FP	FAILS SHETGAONKAR ANAY ANIL
Solid State Devices and Semiconducto Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics	Total: Sex r Physics Theory IA Theory IA Theory IA Theory IA	38 No Of Credits 4 2 4 2 4 2	Nam Graco Obtai FF BC FF CC CC CC CC CC	e: Side ned FP FP FP	FAILS SHETGAONKAR ANAY ANIL
Solid State Devices and Semiconducto Digital VLSI Design Analog VLSI Design VLSI Technology	Total: Sex r Physics Theory IA Theory IA Theory IA Theory IA Theory IA	38 No Of Credits 4 2 4 2 4 2 4 2	Nam Grac Obtai FF BC FF CC CC CC FF CC FF CC	e: Side ned FP FP FP	FAILS SHETGAONKAR ANAY ANIL
Solid State Devices and Semiconducto Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics	Total: Sex r Physics Theory IA Theory IA Theory IA Theory IA Theory IA IA Theory IA	38 No Of Credits 4 2 4 2 4 2 4 2 4 2	Nam Grac Obtai FF BC FF CC CC CC FF CC BB	e: Side ned FP FP FP FP FP FP	FAILS SHETGAONKAR ANAY ANIL
Solid State Devices and Semiconducto Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics HDL - LAB	Total: Sex r Physics Theory IA Theory IA Theory IA Theory IA Theory IA	38 No Of Credits 4 2 4 2 4 2 4 2	Nam Grac Obtai FF BC FF CC CC CC FF CC FF CC	e: Side ned FP FP FP	FAILS SHETGAONKAR ANAY ANIL
Solid State Devices and Semiconducto Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics	Total: Sex r Physics Theory IA Theory IA Theory IA Theory IA Theory IA Theory IA Theory IA	38 No Of Credits 4 2 4 2 4 2 4 2 4 2	Nam Grac Obtai FF BC CC CC CC FF CC BB BB	e: Side ned FP FP FP FP FP	FAILS SHETGAONKAR ANAY ANIL
Solid State Devices and Semiconducto Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics HDL - LAB	Total: Sex r Physics Theory IA Theory IA Theory IA Theory IA Theory IA IA Practical IA	38 No Of Credits 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4 2 4	Nam Grac Obtai FF BC CC CC CC FF CC BB BB BB	e: Side ned FP FP FP FP PP FP FP FP FP FP FP FP FP	FAILS SHETGAONKAR ANAY ANIL
Solid State Devices and Semiconducto Digital VLSI Design Analog VLSI Design VLSI Technology Advanced Engineering Mathematics HDL - LAB	Total: Sex r Physics Theory IA Theory IA Theory IA Theory IA Theory IA Theory IA Theory IA	38 No Of Credits 4 2 4 2 4 2 4 2 4 2	Nam Grac Obtai FF BC CC CC CC FF CC BB BB	e: Side ned FP FP FP FP FP	FAILS SHETGAONKAR ANAY ANIL



COLLEGE: GOA COLLEGE OF ENGINEERING

	R No: 201004714	Se	ex: F	Nam	e :	KANADE SHRADDHA SABAJI	
o Of Attempts: 2			No Of Credits	Grad Obtail		SGPA	
Solid State Devices	and Semiconductor	•					
		Theory	4	CC	+		
		IA	2	AA	+		
Digital VLSI Design							
		Theory	4	BC	+		
		IA	2	BB	+		
Analog VLSI Desigr	1						
		Theory	4	BC	+		
		IA	2	BB	+		
VLSI Technology							
		Theory	4	CC	+		
		IA	2	AB	+		
Advanced Engineer	ing Mathematics						
		Theory	. 4	FF	F		
		IA	2	CC	+		
HDL - LAB							
		IA	2	AA	+	*	
		Practical	2	AA	+		
VLSI CAD - LAB							
		IA	2	AB	+		
		Practical	2	AB	+		
		Total: NO CHAN	38 IGE			6.00 F FAILS	-
	No: 200806910	Sex	k: M	Name	e: :	SATARDEKAR GIRISH GURUDAS	
Of Attempts: 2			No Of Credits	Grade	1000	SGPA	

			140 011	MINOL			FAILS	
No: 3120	PRNo:	200806910		Sex: M	Nam	ie:	SATARDEKAR GIRISH GURI	JDAS
Of Attempts: 2				No Of Credits	Grad Obtai	de	SGPA	
Solid State De	vices and S	emiconductor	Physics	Crounto	Obtai	neu		
			Theory	4	BC	+		
			IA	2	BC	+		
Digital VLSI De	esign							
			Theory	4	BB	+		
			IA	2	BB	+		
Analog VLSI D	esign							
			Theory	4	BC	+		
			IA	2	BB	+		
VLSI Technolo	gy							
			Theory	4	BC	Р		
			IA	2	AB	+		
Advanced Engi	ineering Ma	thematics						
			Theory	4	CC	Р		
			IA	2	CC	+		
HDL - LAB								
			IA	2	BC	+		
			Practica	l 2	BC	+		
VLSI CAD - LA	В							
			IA	2	AB	+		
			Practica	1 2	BB	+		SIVE
•		- 441	Total:	38			6.32 P PASSES	S TALFIGAD

Grade	Grade Points	Performance
AO	10	Outstanding
AA	9	Excellent
AB	8	Very Good
ВВ	7	Good
ВС	6	Fair
СС	5	Satisfactory
FF	0	Fail

Assistant Registrar-E(Proff.)

Controller Of Examinations

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average